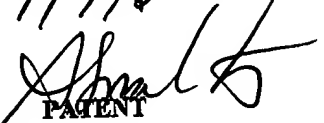


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Attorney for Applicant

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PATENT

Attorney Docket No.: DB000575-012

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Keeth, et al.	}	Examiner:	Anh Quan Tra
Serial No.:	09/885,217		Art Unit:	2816
Filed:	22 August 2001			
Entitled:	256 MEG DYNAMIC RANDOM ACCESS MEMORY			

RESPONSE AND AMENDMENT

In response to the Office action mailed 29 May 2002, please amend the above-identified application as follows:

In the claims

Please cancel claims 224, 497, and 498.

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Please amend the following claims:

223. (Amended) A voltage reference circuit responsive to an external voltage for supplying a reference voltage, comprising:
an active reference circuit for receiving the external voltage and for producing a reference signal having a desired relationship with the external voltage, said active reference circuit comprising a current source utilizing a current mirror for providing current to a diode stack having an adjustable impedance; and
a unity gain amplifier responsive to said reference signal for producing the reference voltage.

225. (Amended) The voltage reference circuit of claim [224]223 wherein said diode stack includes a plurality of transistors connected in series, with each transistor's gate connected to a common potential, and a plurality of switches each for selectively shunting one of said transistors.

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